IMPROVING THE ACTIVE POWER FILTER PERFORMANCE WITH AN ADAPTIVE LINEAR NEURAL NETWORK BASED ON THE REFERENCE GENERATION

Ahmed M. Dabroom
Yanbu Industrial College, Saudi Arabia, Electrical Eng. depart.
dbroommohmed@yahoo.com

ABSTRACT

The use of artificial neural networks (ANNs) in power system problems is increasing attention due to their ability to learn and handle the nonlinearities and uncertainties of the systems. Since the last decade, different ANNs identification and filtering techniques have been applied in power systems and adaptive linear neuron networks (ADALINE) are actually widely used. More recently artificial neural networks have been introduced as a complement or an alternative to traditional control algorithms. The use of neural networks in control applications including process control, robotics, industrial manufacturing and aerospace applications, among others has recently experienced rapid growth. This paper proposes a new control method for shunt active power filter, which is based on ANN. An adaptive linear neuron (ADALINE) will be presented to estimate the reference compensation currents. The resultant compensation currents eliminate harmonics and reactive power compensation with a quick dynamic response. Theoretical and experimental results in a good agreement.

Keywords: Active power filter, Adaptive Linear Neuron (ADALINE), reference current extraction, Digital Signal Processing (DSP).

1. INTRODUCTION

There has been a continuous proliferation of nonlinear type of loads in industry as well as by the consumers. Rectifiers, inverters, AC regulator, and other nonlinear loads introduce voltage and current harmonics in power systems. Currents and voltages are thus non-sinusoidal and it is necessary to compensate the generated harmonics. Active Power Filters (APF) can be employed to compensate harmonics in power systems [1-3]. APF compensation techniques can be easily inserted in existing industrial power systems and are thus widely used. Shunt (APF) compensates current harmonics by injecting equal-but-opposite harmonic compensating current. In this case the shunt active power filter operates as a current source injecting the harmonic components generated by the load but phase shifted by 180°. This principle is applicable to any type of load considered a harmonic source. Moreover, with an appropriate control scheme, the active power filter can also improve power factor at the supply terminals [3-5]. In this way, the power distribution system sees the non linear load and the active power filter as an ideal resistor. The current compensation characteristic of the shunt active power filter is shown in Fig. 1.

In this paper an improved scheme is proposed using Adaptive Linear Neuron (ADALINE). The ADALINE neural network uses to separate the
fundamental component from the distorted supply current. Based on the Fourier series, this new decomposition of current signals allows to define the neural network inputs for which a Least Mean Squares (LMS) algorithm carries out the weights training. The method utilizes adaptive neurons (ADALINE) to process the signals obtained from the line. This current ADALINE extracts the fundamental components of the distorted line current signal and the output of the ADALINE is compared with distorted supply current to construct the modulating signal. This modulating signal is used to generate the PWM pulses that fed to the active power filter to generate compensating current. This compensating current is fed against to the distorted line current. Thus the power quality will be maintained [6-7].

2. ADAPTIVE LINEAR NEURAL NETWORK (ADALINE) PRINCIPLE

Adalines are simple dynamical learning systems by means of a linear combination of time-dependent signals. Determining the value of its internal weights, referred to as the training, is achieved which effective algorithms such as the LMS learning rule. This particular form of nonlinear regression is fast and accurate. The simplicity of its architecture offers to the Adaline some other advantages: the possibility of interpreting its weights and the possibility of hardware implementation. Fig.2 shows an Adaline which is composed of an input vector (X), a weight vector (W) and an activation function f (W). There is a close analogy between artificial neurons and biological neurons. The weight vector W = [W1 W2 .... Wn]T corresponds to the set of synaptic strengths of a biological neuron. The input vector X = [X1 X2 .... Xn]T of the Adaline corresponds to the set of electrical or chemical signals received by the synapses of the dendrites of a biological neuron. The activation function y = f (W) is the mathematical modeling of the behavior of the core of a biological neuron. Different activation functions are possible in neural networks. The Adaline neuron uses linear activation functions so the output (y) is simply the sum of the weighted inputs (y = f (WTX) = W1X1+W2X2+..........+WnXn).

When a neuron is excited it produces an output (y) which depends on its inputs. In an Artificial Adaline Neuron these modifications correspond to weight changes. The (W) vector is constantly modified during the life of the neuron or during the training or learning process. In the theory of artificial neural networks the learning process may be divided in two major categories: on-line and off-line training. The Adaline is an on-line trained neuron, this means that the training process of the Adaline is carried all time. Other kinds of neural networks are trained only once so that the network is effectively used only after a fixed set of synaptic weights is obtained (generally after a very long training process). A learning rule must be found to correctly update (or modify) the weight vector W of an artificial neuron. Since neurons are generally part of a more complex system which is capable to learn or acquire knowledge (the human brain, for instant) the process of synaptic weight updating is called a learning process. One says that a neuron is undergoing a learning process when W is being updated. Adalines are trained by an on-line learning process based on a least mean square...
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(LMS) algorithm which called delta (Δ-rule). The Δ-rule is based on the LMS minimization of a quadrature error. It can be considered a classical problem of minimizing a cost function. This is a very interesting subject that is not in the scope of this paper. The authors recommend the careful reading for a better understanding of the Adaline network and its Δ-rule. For the purposes of this research it is enough to know that the Δ-rule is given by equation (1).

\[ W(k + 1) = W(k) + \alpha \frac{X(k)}{X^T(k) X(k)} \]  

(1)

In equation (1):
- The index \( k \) corresponds to the discrete instant \( kT \)
- \( \alpha \) is the learning rate of the neuron,
- \( \varepsilon \) is the error between the output of the neuron and the desired output \( \varepsilon = y - y_d \)
- \( X(k) \) is the input vector and \( X^T(k) X(k) \) is the norm of the input vector.

The learning rate \( \alpha \) is an important parameter. A high \( \alpha \) increases the learning speed but cannot warranty an accurate learning (i.e. the minimum error between the output and the desired output may be too high). A small \( \alpha \) decreases the learning speed but can provide a small error.

3. ADALINE BASED FOURIER SERIES

In the previous section it was seen that the weights of the Adaline are updated by the Δ-rule so that the error \( \varepsilon \) is minimum. This means that the output \( y \) of the Adaline is forced to follow a desired output \( y_d \). If \( y_d \) is a time-varying signal the adapting or learning process must be carried in real time and must be fast enough in order to produce a small error at every time instant. As discussed previously, a reasonably high (but small enough to provide a small error) value of \( \alpha \) allows a fast convergence, thus the Adaline weight vector \( W \) can be adapted in real time so that \( y = y_d \) even if \( y_d \) changes with time and is not periodic. After a brief adapting process the weight vector \( W \) becomes quasi stationary (because \( \varepsilon \) becomes low and the weight increases or decreases become small) if \( y_d \) is periodic, i.e. the Adaline learns how to generate the output \( y \) so that \( y \approx y_d \). Whenever the waveform of \( y_d \) is altered another adapting process is started so that a new vector \( W \) is determined (with a low \( \varepsilon \) at the end of the process), i.e. the Adaline need to learn again. The adaptive behavior of the Adaline can be used to find the Fourier Series approximation of the signal \( y_d \). This can be achieved if the inputs of the Adaline are sine and cosine functions of fundamental and harmonic frequencies. Fig.3 shows how this is done. A periodic waveform can be expanded by Fourier analysis of the time function \( y_d \) is given by equation (2). The series contains \( N \) oscillatory components and one constant component. The trigonometric functions have angular frequencies \( \omega n \) where \( n \) is the harmonic order \( (n = 1 \) corresponds to the fundamental frequency).

\[ y_d(t) = A_0 + \sum_{n=1}^{N} C_n \sin(n \omega t + \varphi_n) \]  

(2)

Equation (2) may be rewritten as (3), where each oscillatory component is expressed as a sum of two trigonometric functions.

\[ y_d(t) = A_0 + \sum_{n=1}^{N} A_n \sin(n \omega t) + \sum_{n=1}^{N} B_n \cos(n \omega t) \]  

(3)

The relation between the amplitudes of the oscillatory terms of Equations (2) and (3) is given by equation (4) and the displacement angle \( \varphi_n \) is given by equation (5). Equation (5) is not used in the compensation method proposed in this paper because the Fourier series in the form of equation (3) is used. Equation (4) is used because the compensation method needs to determine the amplitudes of the fundamental electric currents, as will be explained later.

\[ C_n = \sqrt{A_n^2 + B_n^2} \]  

(4)

Fig.3 Fourier series with (ADALINE)
\[ \varphi_n = \tan^{-1} \frac{B_n}{A_n} \]  

(5)

Equation (6) is the discrete form of equation (3), because the compensation method is intended to be implemented in digital processors. It is convenient to write equation (6), where the index \( k \) corresponds to the \( k \)-th discrete time instant. The sampling interval \( T \) is the same used in the \( \Delta \)-rule of Equation (1). When the Adaline of Fig.3 is used to determine the Fourier series of \( y_d \) the weight vector \( W \) undergoes an adaptive updating process with the \( \Delta \)-rule of equation (1). If the error \( \varepsilon \) is sufficiently small the components of the weight vector are the coefficients of the Fourier series of the time function \( y_d \).

\[ y_d(t) = A_0 + \sum_{n=1}^{N} A_n \sin(n\omega kT) + \sum_{n=1}^{N} B_n \cos(n\omega kT) \]

For simplicity, the input vector \( X \) (in the discrete form) and the weight vector \( W \) of the Adaline of Fig.(2) are written in equations (7) and (8).

\[ X = \begin{bmatrix} 1 \sin(\omega kT) & \cos(\omega kT) & \sin(2\omega kT) & \cos(2\omega kT) & \ldots & \sin(N\omega kT) & \cos(N\omega kT) \end{bmatrix}^T \]

(7)

\[ W = \begin{bmatrix} A_0 & A_1 & B_1 & A_2 & B_2 & \ldots & A_N & B_N \end{bmatrix}^T \]

(8)

4. DETERMINATION OF REFERENCE COMPENSATING CURRENTS

A- General description of the system
The basic principle of the proposed control system to get the reference compensating currents of shunt APF is shown in Fig.4, where the figure shows the basic elements of control system. This scheme is mainly composed of an adaptive linear neural network (Adaline), phase locked loop circuit (PLL) and hysteresis current controller. The adaptive ANN extraction circuit, hysteresis control and the PLL circuit are integrated as an overall mode of adaptive shunt APF. In this circuit, the Adaline network will take information from the nonlinear load currents to estimate the reference compensation currents by identifying the harmonics with a Fourier expansion of the load currents and direct component of supply voltages. The PLL circuit generates sinusoidal signals with constant unit amplitude, even if the supply voltage has a distortion. Finally, the reference compensating currents of shunt APF are compared with its actual and the error fed to hysteresis current controller to obtain the switching of voltage source inverter (VSI) for injecting phase-opposite the harmonic currents in the power system. The following subsection outline the detailed control schemes of the proposed system.

![Fig.4 Basic elements of control system.](image)

B- Learning with Adalines
The harmonic and reactive compensation of the proposed system using Adaline-based approach is outlined as follows:

Considering phase (a) as studying case using Fourier series, the nonlinear load current waveform can be represented by equation (9):

\[ i_L(t) = \sum_{n=1}^{N} \left( i_n \sin(n\omega t + \theta_n) \right) \]

(9)

Where:
- \( n \): harmonic order;
- \( N \): order of highest harmonic;
- \( i_n \): magnitude of \( n \)th harmonic;
- \( \theta_n \): phase angle of \( n \)th harmonic

In harmonic extraction procedure, the equation (9) can be expanded as follows:

\[ i_L(t) = \sum_{n=1}^{N} \left( i_n \sin(n\omega t) \cos(\theta_n) + i_n \cos(n\omega t) \sin(\theta_n) \right) \]

(10)

\[ = i_1 \sin(\omega t) \cos(\theta_1) + i_1 \cos(\omega t) \sin(\theta_1) + \sum_{n \neq 1} \left( i_n \sin(n\omega t) \cos(\theta_n) + i_n \cos(n\omega t) \sin(\theta_n) \right) \]

Where:
- The first term without summation corresponds to fundamental frequency;
I₁ and φ₁ are the magnitude and phase angle, respectively.

- The second term with summation corresponds to nᵗʰ harmonic from n=2 to n = N, and similarly, Iₙ and φₙ are the magnitude and phase angle, respectively.

Equation (11) can be written as:

\[ i_{k}(t) = W_{1}\sin(\omega t) + W_{2}\cos(\omega t) + W_{3}\sin(2\omega t) + W_{4}\cos(2\omega t) + \cdots + W_{n}\sin(n\omega t) + W_{2n}\cos(n\omega t) \]  

(12)

Where:

\[ W_{1} = I_{1}\cos(\phi_{1}) \quad \text{and} \quad W_{2} = I_{1}\sin(\phi_{1}) \]

Equation (13) is the discrete form of equation (12)

\[ i_{k}(k) = W_{1}\sin(\omega kT) + W_{2}\cos(\omega kT) + W_{3}\sin(2\omega kT) + W_{4}\cos(2\omega kT) + \cdots + W_{n}\sin(n\omega kT) + W_{2n}\cos(n\omega kT) \]  

(13)

The proposed control method is based on three Adaline neurons (Adaline neuron for each phase), but for simplicity, the studying will be investigated in one phase. To construct the adaptive ANN (Adaline) for one phase, the equation (13) is used and according to equations (7) and (8) from the previous section, the input vector X to the adaptive ANN is first determined as:

\[ [X]^T = [\sin(\omega kT) \quad \cos(\omega kT) \quad \sin(2\omega kT) \quad \cos(2\omega kT) \cdots \sin(n\omega kT) \quad \cos(n\omega kT)] \]

and the weight vector W of the Adaline is:

\[ [W]^T = [W_{1} \quad W_{2} \quad W_{3} \quad \cdots \quad W_{2n-1} \quad W_{2n}] \]

Fig. 5 describes the Adaline structure with the input vector (X) being the AC components and the weights (W) correspond to the amplitudes of each harmonic current multiplied with alternatively cos φ or sin φ. As shown in Fig. 5, the output of the neuron is sum of the product W^T X, which itself is estimated current (iₐₑ) of nonlinear load. The error εₐ is the difference between the output of the neuron (iₐₑ) and the measured nonlinear load current (iₐₑ). This error signal is then propagated backward through the network, against the direction of synaptic connection. The synaptic weights are adjusted so as to make the actual response of the network move close to the desired response. The learning process performed with the error back-propagation algorithm is called back-propagation learning. Back propagation was created by “Widrow” (the Least Mean Square LMS). The Least Mean Square (LMS) algorithm can be considered a special case of the Δ-learning rule, it is independent of the activation function of neurons. Based on the LMS learning rule this model represents the simplest intelligent self-learning system that can adapt itself to achieve a given linear modeling task. The problem consists in finding a suitable set of weights such that the input-output behavior of the Adaline becomes close to a set of desired input-output data points linked with a linear relationship. The Adaline weights are solved using an iterative linear LMS algorithm in order to minimize the estimation error. The LMS algorithm is designed with a single linear neuron model see (Fig.5); in this case the design of the LMS algorithm is very simple. The Adaline weights are adjusted according to:

\[ W_{k+1} = W_{k} + \alpha \varepsilon_{k} X(k) \]  

(14)

Where α is learning rate or called convergence coefficient chosen between 0 and 1. As shown by Fig. 5, the amplitude of fundamental current Iₚₐ for phase (a) is extracted from the Adaline’s weights using the first and second elements of the vector W of the Adaline as follows:

\[ I_{p\text{a}} = \sqrt{W_{1}^{2} + W_{2}^{2}} \]  

(15)

A similar situation takes place in other Adalines for phases a,b and hence three values of the amplitudes of fundamental currents Iₚₐ, Iₚₙ and Iₚₙ can be obtained. These three values are averaged to balance the system written as;

\[ I_{p\text{ave}} = (I_{p\text{a}} + I_{p\text{n}} + I_{p\text{c}})/3 \]
To achieve the target of pure sinusoidal wave shape in source current with a nearly unity power factor, the dc capacitor regulator with PI control is used as a basic element of the proposed control as shown in Fig.5. In order to regulate the dc-link voltage of the voltage source inverter (VSI) and keep constant, the sensed dc-link voltage is subtracted by its reference value, and then the error signal is processed in the PI control. The output of the dc-link voltage controller is $\Delta i$, which is necessary to regulate the amplitude of the fundamental sinusoidal current. Then the averaged of amplitudes of fundamental currents ($I_{pavg}$) is added to the output of the dc-link voltage controller ($\Delta i$) and the result is multiplied with the output of PLL circuit in order to generate the active component current (in-phase with supply voltage) for every phase as follows:

$$
\begin{align*}
I_{pa} &= (I_{pavg} + \Delta i) \sin(\omega t) \\
I_{pb} &= (I_{pavg} + \Delta i) \sin(\omega t - 120^\circ) \\
I_{pc} &= (I_{pavg} + \Delta i) \sin(\omega t + 120^\circ)
\end{align*}
$$

Finally, the estimation load currents, which are output of Adaline neurons ($i_{Lest}$) are subtracted from the active component currents $i_p$ (in-phase with supply voltages), the reference currents of the shunt APF can be obtained the following equation.

$$
i_{ca}^* = i_{pa} - i_{Lest}
$$

If the nonlinear load current $i_L$ is presented by:

$$
i_L = i_p + i_q + i_h
$$

Where $i_p$ represents the active current, $i_q$ represents the reactive current and $i_h$ represents the sum of all harmonic currents. If the shunt APF, which is connected in parallel with nonlinear load, can generate $i_c$, which is opposite but equal ($i_q + i_h$), then the supply only needs to the active current $i_p$. This active current is a sine wave and in phase with the supply voltage $v_s$. Consequently, to produce the reference current of the shunt APF $i_c^*$ first, the reference supply, which is represented to active component current $i_p$, must be calculated as discussed before by proposed control algorithm.

5. SIMULATION RESULTS OF PROPOSED ADALINE

The simulation of the proposed Adaline is carried on MATLAB/ Simulink as represented in the Fig.5. Simulation results have been achieved with Adaline algorithm, under different conditions of main voltage and another more disturbance when using nonlinear load such as: three phase controlled rectifier at specific firing angle of thyristor. Also the compensating of shunt APF with multiple loads is examined and discussed.

A number of simulation results with different operating conditions were developed. In Fig.6, shunt APF is connected in parallel with different nonlinear loads, the three-phase controlled rectifier by switch SW₁ and induction motor by switch SW₂.

5.1 Three-phase controlled rectifier:

When switch sw₁ in Fig.6 is only closed, and then the three-phase controlled rectifier with resistive load has to be compensated by the APF. Unfortunately, this controlled rectifier has the inherent problem that the power factor decreases as the firing angle
increases and that harmonics of the supply current are high. Since the compensation of the shunt APF depends on the firing angle ($\alpha$) of the rectifier, two different values for the firing angle $\alpha = 0^\circ$ and $\alpha = 40^\circ$ have been considered.

5.1.1 Ideal mains voltage case and firing angle $\alpha = 0^\circ$

The waveforms obtained with Adaline algorithm in this case are presented in Fig. 7. The supply current $i_{sa}$ and its spectrum before compensation are shown in Fig. 7-a,b. On the other side the supply current $i_{sa}$ and its spectrum after compensation are shown in Fig. 7-c,d. Fig. 7-e shows the fundamental active component current for phase (a) $i_{pa}$. While Fig. 7-f shows the averaged of amplitudes of fundamental currents ($I_{pavg}$). The shunt APF current $i_c$ composed to its reference current $i_{c*}$ and the error between them are presented in Fig. 7-g,h. The capacitor voltage and its reference are shown in Fig. 7-j. As can be remarked, the results have shown that, the supply current after compensation is almost sinusoidal and in phase with corresponding voltage. Also, good tracking of the shunt APF actual current for its reference. Total harmonic distortion (THD) has been calculated of the supply current before and after compensation. The figures and calculation show that THD improved from 22.8893% (before compensation) to 0.08% (after compensation).
5.1.2 Simulation results at firing angle $\alpha = 40^\circ$:

Another case with more distortion in the nonlinear load current is discussed at $\alpha = 40^\circ$. Fig.8, shows the simulation results when the firing angle of controlled rectifier $\alpha = 40^\circ$, to change of nonlinearity of the load. Fig.8-a shows the supply current $i_{sa}$ without APF. The supply current $i_{sa}$ with APF and the supply voltage $v_{sa}$ are shown in Fig.8-b. Figure 8-c shows the fundamental active component current $i_{sa}$, which is in phase with supply voltage. Also Fig.8-d shows the averaged of amplitudes of fundamental currents ($I_{pavg}$). The APF current $i_c$ compared with its reference current $i_c^*$ and the current error $E$ between them are shown in Fig. 8-e and Fig. 8-f respectively. The capacitor voltage $V_C$ compared with its reference $V_C^*$ are shown in Fig.8-g. Figures 8-h,j show the source current $i_{sa}$ spectrum referred to the amplitude of the fundamental current before and after applied APF. Total harmonic distortion (THD) has been calculated for the current before and after compensation. The calculation shows that THD has been improved from 25.7017 $\%$ without the APF to 1.5177 $\%$ with the APF.

![Fig.8-a](image1)
![Fig.8-b](image2)
![Fig.8-c](image3)

5.2 Multiple loads

In industrial application shunt APF may be connected in parallel with different nonlinear loads as shown in...
Fig. 6, the three-phase controlled rectifier by switch SW₁ and induction motor by switch SW₂, the induction motor regularly has a low lagging power-factor at run-up and steady state operation. To compensate these undesirable effects, shunt APF is generally used as a reactive power source during both run-up and running, and consequently the power factor will be improved. From Fig. 6 when the switch SW₁ is still on and switch SW₂ closed, then the three-phase controlled rectifier with resistive load (in case of \( \alpha = 0^\circ \)) and induction motor act as a multiple load, which have to be compensated by the shunt APF. Fig. 9 shows the simulation results of a multiple load, where the Fig. 9-a shows the induction motor current (\( i_{ma} \)) with its respective supply voltage \( v_{sa} \), while Fig. 9-b shows the supply current \( i_{sa} = i_{La} + i_{ma} \) before compensation. The supply current \( i_{sa} \) with supply voltage \( v_{sa} \) after compensation is shown in Fig. 9-c. As can be seen from the figures, the induction motor current \( i_{ma} \) has a lagging power-factor during the running period and the supply current \( i_{sa} = i_{La} + i_{ma} \) of the multiple load before compensation is non-sinusoidal and include distortion. The supply current \( i_{sa} \) after compensation became sinusoidal and in phase with its respective voltage \( v_{sa} \). It is proved that, the shunt APF reduces the reactive power drawn from the source and improves the power-factor. Fig. 9-d,e shows current spectrum of the supply current \( i_{sa} \) referred to the amplitude of the fundamental before and after compensation, where the Fig. 9-d shows the source current \( i_{sa} \) spectrum before compensation and Fig. 9-e depicts the source current \( i_{sa} \) spectrum after compensation. In order to evaluate the good performance of the control, total harmonic distortion (THD) is taken account before and after compensation. The calculation shows that THD has been improved from 20.1515% without the shunt APF to 1.01% with the shunt APF.

5.3 Induction motor direct on line start up
A direct on line start up of an induction motor can cause a significant voltage dip, and the power factor during a direct on line start is small while the start up apparent power is often very high in comparison with the rated power. Traditionally compensation the reactive power is performed by switched capacitor banks, which are connected in shunt with the load. This technique however is slow, inaccurate and involves bulky and expensive capacitors. Since the shunt APF is more flexible and has higher speed of response, the simulation results of the starting current of the induction motor, with and without compensation are compared to investigate the effectiveness of the compensation performed by the shunt APF. Fig. 10 shows the waveforms during the start up of induction motor. Fig. 10-a and Fig. 10-b shows the supply current \( i_{s} \) with its respective supply voltage \( v_{s} \) of an induction motor without and with compensation respectively. Whereas Fig. 10-c shows the performance of the averaged of amplitudes of fundamental currents \( (I_{pavg.}) \) while start up of induction motor. The results have shown that the supply current after compensation is almost in phase with the supply voltage, thus, an improvement in the power factor is achieved.
Fig. 10-a The supply current $i_s$ with its respective voltage during run-up before compensation

Fig. 10-b The supply current $i_s$ with its respective voltage during run-up after compensation

Fig. 10-c Performance of the averaged of amplitudes of fundamental currents ($I_{pav}$) while start up of induction motor.

6. EXPERIMENTAL TESTS

The three-phase shunt active power filter laboratory prototype has been implemented with the specified system parameters in Appendix. The proposed control algorithm is implemented using digital signal processor DSP (DS1104) manufactured by DSPACE company and developed under the integrated development environment of MATLAB-SIMULINK provided by the MATHWORK. The experimental circuit configuration is shown in Fig. 11.

A variety of waveforms show the experimental test results of the prototype for proposed method. Fig. 12 depicts the performances of source current before and after compensation when the firing angle of controlled rectifier $\alpha = 0^\circ$.

While Fig. 13 shows the source current before and after compensation when the firing angle of controlled rectifier $\alpha = 40^\circ$. 

Fig. 11 Experimental arrangement setup of shunt APF using DSP

Fig. 12 Performances of source current before and after compensation at firing angle $\alpha = 0^\circ$.

Fig. 13 Performances of source current before and after compensation at firing angle $\alpha = 40^\circ$.
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Through experimental results, it is revealed that the supply currents after compensating were found to be sinusoidal and in phase with its respective supply voltage, which was consistent with the simulation shown above. For this test, the outcome indicates that the proposed method is useful for the active filter studies and it indicates the effectiveness of the proposed controller for shunt APF under different conditions.

7. CONCLUSION

A new strategy to estimate harmonic distortion from nonlinear load current has been proposed. An Adaline neural network has been presented to determine precisely the necessary currents in order to cancel harmful harmonics. The proposed strategy was based on the Fourier series analysis of the current signals and LMS training algorithm carries out the weights. An experimental shunt APF has been carried out on DSP to explore the advantages and practical implementation with the proposed control strategy. The simulations and experimental results have proven good performances and verify the feasibility of the proposed algorithm, and it is most effective under different conditions of nonlinearity of industrial applications.

8. REFERENCES


Appendix A

The parameters of the Shunt APF

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltages</td>
<td>$v_s = 380$ volt</td>
</tr>
<tr>
<td>Rectifier load resistance</td>
<td>$R_{dc} = 25 , \Omega$</td>
</tr>
<tr>
<td>Rectifier load current</td>
<td>$i_L = 20$ Amp.</td>
</tr>
<tr>
<td>SAF inductor</td>
<td>$L_f = 4$ mH</td>
</tr>
<tr>
<td>SAF DC link capacitor</td>
<td>$C = 250 , \mu$F</td>
</tr>
<tr>
<td>SAF DC link voltage</td>
<td>$V_c = 750$ volt</td>
</tr>
<tr>
<td>Grid frequency</td>
<td>$f= 50$ Hz</td>
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